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## APPLICANTS

Koichiro Narimatsu, Hyogo, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/570,463 05/12/2000 PAT 6,596,603

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 11-346079 (P) 12/06/1999

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 09/12/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 4	TOTAL CLAIMS 11	INDEPENDENT CLAIMS 2
Verified and Acknowledged Examiner's Signature	Initials				

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## TITLE

Semiconductor device and manufacturing method thereof, and registration accuracy measurement enhancement method

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